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voltage simulation mentor graphics Hi, I'm using ELDO for analog and digital simulation. Mostly, after parasitic extraction. I'll re-simulate my design and that's it. I never done any impedance plotting before. But I'll try to help you. Added after 47 minutes: Hi, I found this article in Mentor's Supportnet. I hope it useful to you.

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WILSONVILLE, Ore., March 5, 2009 – Mentor Graphics Corporation (NASDAQ: MENT) today announced that STMicroelectronics (), a global leader in developing and delivering semiconductor solutions across the spectrum of microelectronics applications, has used the Mentor Graphics Eldo® circuit simulator to characterize its first CMOS 32nm cell libraries.

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The subjects covered are similar to the ones in the Eldo Platform Essentials Training for all what is about pure analog simulation, but with an extra layer to present how to use most of these features through the Artist link interface, so dedicated to users not familiar with Artist Link and who are using in Cadence ADE PDKs delivered for Eldo.

Eldo RF User's Manual - BME EET

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Fortunately, Eldo® Premier from Mentor Graphics® has a superb transient noise analysis feature that achieves both accuracy and performance when simulating large scale analog/mixed signal circuits for such effects. Eldo Premier simulation results demonstrate a high correlation with real silicon evaluation results. ”

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